

SEMI F47 Voltage Sag Immunity Test Report for Power Supply CP20.241-ETC



Other devices covered by this report:

CP20.248	Unit with build-in display	
CP20.242-IOL	Unit with IO-Link interface	
CP20.248-IOL	Unit with IO-Link interface and build-in display	

SEMI F47 Test Report



Document number	CP20.241-ETC Semi F47 Rev2 TR1	
PCTM number	PCTM-27 (2)	
Standards	SEMI F47-0706 (July 2006) SPECIFICATION FOR SEMICONDUCTOR PROCESSING EQUIPMENT - Voltage Sag Immunity Compliance Tests	
	techniques - Voltage dip	1:2017 ibility (EMC) - Part 4-11: Testing and measurement s, short interruptions and voltage variations ment with input current up to 16 A per phase
Applicant	PULS GmbH Elektrastraße 6 81925 Munich, Germany	<i>,</i>
Test Laboratory	PULS Vario GmbH Kranichberggasse 6 1120 Vienna, Austria	
Test Engineer	Thomas Ramel, Daniel Schwayer	
Test Date	30.08.2023	
Description of Test Device:	Built-in power supplies for DIN-Rail mounting	
Devices under Evaluation:	CP20.241-ETC Power Supply 1-Phase, 24V, 20A, 480W	
S/N of Devices:	CP20.241-ETC: S/N: 2594	15900 A
Application Details:	Input voltage: Input frequency: Output load:	1-Phase AC 208V 50 or 60Hz 480W



PASS/FAIL Criterions:

Test Result:

PASS

value

The test device passed all essential SEMI F47-0706 tests according to the defined application details without any limitations and is qualified to bear the following approval mark:

The output voltage is not allowed to deviated more than 5% of the initial

In accordance with paragraph 7.8.2 a) of SEMI F47-0706



Since DC power supplies, as covered in this test report, are only components of a semiconductor processing equipment, the tests of the SEMI F47 standard were conducted with selected rated characteristics of the DC power supply.

The system integrator of the final semiconductor processing equipment needs to judge if the results of this test report are compatible with the SEMI F47 requirements of his system or if test data under other operating conditions are additionally required.

The system integrator also needs to judge if the results of the inrush current peaks are compatible with the selected external fuses for input protection.

The system integrator also needs to be aware about aging effects. It is expected that the ride through time can be reduced by 15% at end of the specified lifetime expectancy.

A SEMI F47 certificate is not intended for this type of component, however the product fulfils the general requirements and can be marked with the following symbol.

Approved

Harald Etlinger Head of Product Compliance PULS Vario GmbH, Vienna

Date of Approval

14.11.2024



Copy of marking plate:

0		
	CP20.241-ETC Power Supply 1-Phase, 24V, 20A, 480W Built-in EtherCAT slave communication	
	Installation by professional personnel only Read CP20.241-ETC installation manual	
	AC 120-240V, DC 150-300V IN. AC 120-240V, 5.2-2.6A, 50-60Hz DC 150-300V, 41-2-1 OUT: DC 24-28V, 24.0-20.6A (below +45°C) DC 24-28V, 20.0-17.1A (at +60°C) Derate linearly 12.5W/K > +60°C ambient	
	AC 100V, DC 110-150V IN: AC 100V, 5.5A, 50-60Hz DC 110-150V, 4.9-3.6A OUT: DC 24-28V, 21.0-18.0A (below +45°C) DC 24-28V, 17.5-15.0A (at +60°C)	
	Derate linearly 10.5W/K > +60°C ambient	(99)
	Operational temperature range: -25°C to +70°C	
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		45900
	PULS GmbH, Elektrastr. 6 81925 Munich, Germany www.pulspower.com Designed in Germany Assembled in the Czech Republic	IM-457.920.10B

List of Test Equipment

Туре	Model	Inventory number
Test generator:	Kikusui PCR3000WE2	10372
Load:	Chroma 63201 - 2.6kW	10054
Oscilloscope:	LeCroy WS454	10129
Oscilloscope:	LeCroy WS454	10127
Diffential probe.	LeCroy AP031	10259
Current Probe:	LeCroy CP150	10280
Communication Interface:	EtherCAT Testdevice	(MUC) 800.20 [2]

The test equipment complies with the requirements of IEC 61000-4-11.

The peak current capability of the test generator was evaluated according Annex A of IEC 61000-4-11 and is able to deliver minimum 32.7A.

Test Specification for SEMI F47 compliance

Voltage Sag Immunity according to the following table:

Sag depth#1	Duration	Duration at 50 Hz	Duration at 60 Hz
50%	200ms	10 cycles	12 cycles
70%	500ms	25 cycles	30 cycles
80%	1000ms	50 cycles	60 cycles

#1 Sag depth is expressed in percent of remaining nominal voltage. For example, during a 70% voltage sag on a 200 volt nominal system, the voltage is reduced during the sag to 140 volts and not 60 volts.



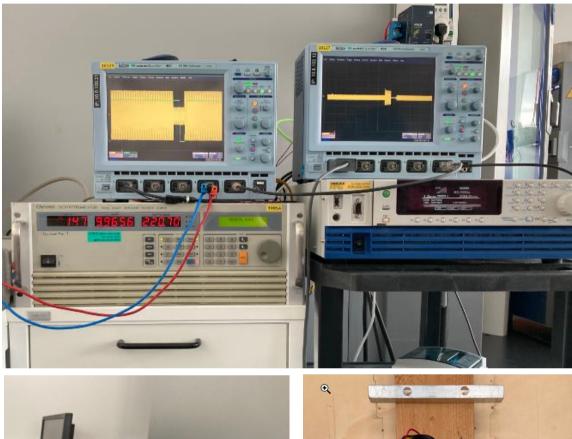
Test Setup

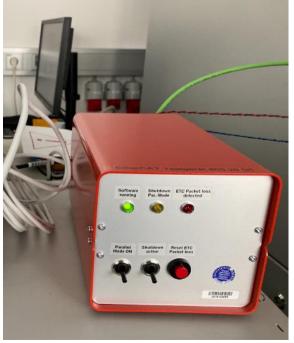
The unit under test in normal operating condition mounted in climate chamber.

The input is connected to an AC Source. The input voltage is measured with a 100:1 differential probe and the input current is measured with current probes. These probes are connected to oscilloscopes.

The output is connected to an active load. The output voltage is connected directly to the oscilloscope. Input and output voltages are measured with oscilloscope #1 and input currents with oscilloscope #2.

Active EtherCAT communication is simulated with the "EtherCAT Testdevice 800.20 [2]" during the tests.

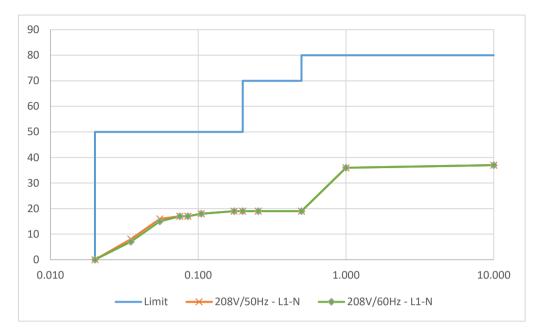


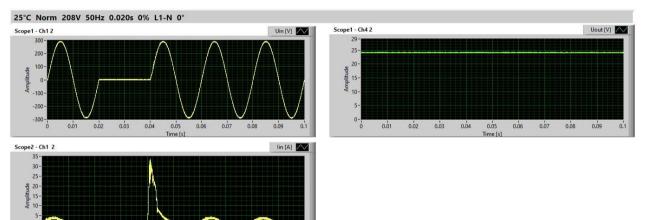


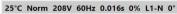




Voltage Sag Results







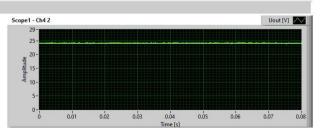
0.02 0.03

0.01



0.04 0.05 Time [s] 0.06 0.07

0.08 0.09





Conducted Tests at 208V 50Hz

Input voltage	208Vac
Input Frequency	50Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Sag duration [s]	Voltage remaining [%]	Positive peak current	Negative peak current
0.020	0	33.7	-4.2
0.200	50	12.2	-8
0.500	70	7	-5.6
1	80	6.1	-4.7
10	80	5.6	-4.7

Informational measurements

Sag duration [s]	Voltage remaining [%]
0.020	0
0.035	8
0.055	16
0.075	17
0.085	17
0.105	18

Sag duration [s]	Voltage remaining [%]
0.175	19
0.200	19
0.255	19
0.500	19
1	36
10	37

Conducted Tests at 208V 60Hz

Input voltage	208Vac
Input Frequency	60Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Sag duration [s]	Voltage remaining [%]	Positive peak current	Negative peak current
0.016	0	29.5	-12.7
0.200	50	12.7	-8
0.500	70	7	-5.6
1	80	5.6	-5.2
10	80	6.1	-4.7

Sag duration [s]	Voltage remaining [%]
0.016	0
0.035	7
0.055	15
0.075	17
0.085	17
0.105	18

Sag duration [s]	Voltage remaining [%]
0.175	19
0.200	19
0.255	19
0.500	19
1	36
10	37



Inrush current measurements according 61000-4-11 at 208V 50Hz

Input voltage	208Vac
Input Frequency	50Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Peak input current measurements on unit under test:

First two measurements turn off input power for EUT for 5 minutes and then

	Measure peak input current when AC turned on at 90°:	5.6
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Measure peak input current when AC turned on at 270°: 5.6

Next two measurements turn on the input power for EUT for at least 1 minute then turn off input power for 5s and on again.

Measure peak input current when AC turned on at 90°:	6.1
Measure peak input current when AC turned on at 270°:	7.5

Inrush current measurements according 61000-4-11 at 208V 60Hz

Input voltage	208Vac
Input Frequency	60Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Peak input current measurements on unit under test:

First two measurements turn off input power for EUT for 5 minutes and then Measure peak input current when AC turned on at 90°: 8

medsure peak input current when he turned on at 50.	0
Measure peak input current when AC turned on at 270°:	6.6
on again.	

Measure peak input current when AC turned on at 90°:7Measure peak input current when AC turned on at 270°:7



Operating conditions and their influence in test results:

a) Ambient temperature:

Control measurements show that the ambient temperature has only a minor influence in the ride-through time test results.

Depending on the used topology to reduce the input inrush current, the ambient temperature can have a major influence in the arising peak current after the sag test. Therefore, tests were performed at ambient temperatures of 25°C and +60°C.

It is assumed that semiconductor processing equipment is never used at lower temperatures than +25°C. Although the power supply itself is specified down to -25°C, a test at such low temperatures is not performed.

b) Mains frequency 50Hz vs. 60Hz:

Control measurements show that 50Hz testing is more critical than 60Hz testing. Therefore, unless otherwise noted, all tests were performed with a mains frequency of 50Hz.

c) Output voltage 24V vs. 28V:

The ride-through time depend on the stored energy in the input capacitors and the amount of output power. The output voltage is not essential as long as the output power is constant.

The adjusted output voltage has no influence in input currents peaks after input voltage sags. Therefore, unless otherwise noted, all tests were performed with an output voltage of 24Vdc.

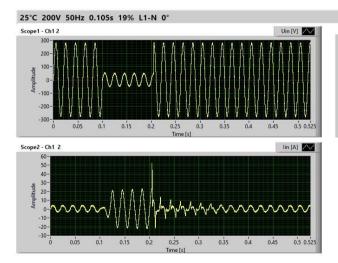


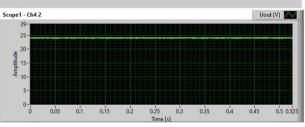
APPENDIX

Informational measurements at 200V

Input voltage	200Vac
Input Frequency	50Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Sag duration	Voltage remaining	Positive peak current	Negative peak current
[s]	[%]	[A]	[A]
0.020	0	34.2	-5.2
0.035	10	23	-50.2
0.055	16	20.2	-50.2
0.075	18	23	-50.6
0.085	18	49.7	-22
0.105	19	52.5	-22
0.175	19	23.9	-50.6
0.200	20	37.5	-22.5
0.255	20	23.9	-46.9
0.500	20	37	-23
1	37	18.7	-11.2
10	39	17.8	-10.8



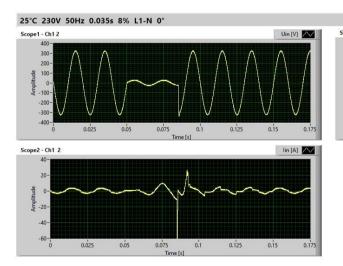


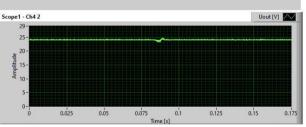


Informational measurements at 230V

Input voltage	230Vac
Input Frequency	50Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Sag duration [s]	Voltage remaining [%]	Positive peak current [A]	Negative peak current [A]
0.020	0	35.2	-3.7
0.035	8	26.7	-60
0.055	14	26.2	-60
0.075	15	27.2	-60
0.085	16	59.5	-23.4
0.105	16	55.3	-23.9
0.175	17	23.4	-51.6
0.200	17	35.6	-23
0.255	17	24.4	-51.1
0.500	17	34.2	-23.4
1	32	20.2	-11.7
10	33	21.6	-10.8







Informational measurements at 100V

Input voltage	100Vac
Input Frequency	50Hz
Output voltage	24V
Output current	20A
Ambient temperature	25°C

Sag duration [s]	Voltage remaining [%]	Positive peak current [A]	Negative peak current [A]
0.000	0	0	0
0.020	0	30.9	-12.2
0.020	0	30.9	-12.2
0.035	17	30	-40.3
0.055	30	30.5	-38
0.075	34	30.5	-38.4
0.085	35	39.8	-28.6
0.105	36	39.4	-29.1
0.175	38	30	-38.9
0.200	38	31.4	-23.9
0	50	23	-18.3
0	39	30	-38.9

